

INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO-1449	Attorney Docket No. 2885/85	Serial No. 10/791,501
	Applicant(s) Martin VORBACH et al.	
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
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						YES	NO
	198 22 776*	03-25-1999	DE				X
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* English-language Abstract provided.

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EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Asari, et al., "FeRAM circuit technology for system on a chip," <i>Proceedings First NASA/DoD Workshop on Evolvable Hardware</i> , pp. 193-197 (1999)
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	Pirsch, et al., "VLSI implementations of image and video multimedia processing systems," <i>IEEE Transactions on Circuits and Systems for Video Technology</i> 8(7): 878-891 (Nov. 1998)
	Salefski, et al., "Re-configurable computing in wireless," <i>Annual ACM IEEE Design Automation Conference: Proceedings of the 38th conference on Design automation</i> , pp. 178-183 (2001)
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EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	